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Teseda Corporation Launches the V550

Desktop silicon validation and diagnostic platform ships to first customers

PORTLAND, OR-October 9, 2007 – Teseda Corporation, a leading provider of comprehensive scan-based diagnostic and debug solutions for semiconductor yield improvement, announced that it has launched and is shipping its next generation desktop validation and diagnostic platform, the V550, to key customers. The V550 is part of a unified system that integrates design, test, and diagnostic environments, leading to the rapid detection and repair of failures.

“The V550 desktop platform, in conjunction with Teseda’s powerful software solutions, allow Design and DFT engineers to quickly validate initial silicon with their test programs, thus saving weeks in the silicon verification process,” stated Armagan Akar, Teseda’s CEO. “Teseda is providing a comprehensive diagnostic system that quickly delivers failure information back to engineers in a language that relates to the exact hierarchy of the design. What is especially unique about this next generation platform is that several customers helped us design the V550 according to their specific requirements. Shipping it now after a rapid one year development effort to those customers is extremely gratifying.”

The V550 now becomes the most powerful desktop validation platform in the industry, able to handle devices with up to 512 channels and providing more memory per channel than any other desktop system. When driven with Teseda’s powerful TWB software, it allows design teams to drastically cut validation and diagnostic time, and FA teams to quickly pinpoint failure candidates.

The V550 also drives Teseda’s Diagnostic Manager (DM) suite of software tools, ScanXY, CellXY and SecureXY. The DM tools quickly link logical test mismatch data to the physical domain for rapid identification of defects and hot spots. ScanXY identifies the XY location of mismatched scan cells, while CellXY identifies the XY location of failing gate candidates. SecureXY works with both to deliver IP secure XY location data back to the device manufacturers. Teseda has created a powerful system with the V550, TWB and DM software that speeds analysis of failing devices within hours, not days.

Teseda will show V550 and the full line of diagnostic software at the 2007 International Test Conference.

About Teseda Corporation

Teseda provides a unifying software and hardware environment that allows multiple engineering disciplines to accelerate the delivery of first silicon. Teseda's software solutions deliver comprehensive diagnostic and debug capabilities that resolve yield limiting failures, reducing the time to volume production. Teseda is headquartered in Portland, Oregon. For more information about Teseda, its products and distribution network, please call 503.223.3315 or visit www.teseda.com

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